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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/623,655 07/22/2003		Tomoji Hamada	2003-0996A 4745	
513	7590 10/28/2005		EXAMINER	
	TH, LIND & PONAC	IM, JUNGHWA M		
2033 K STRE SUITE 800	ETN. W.		ART UNIT	PAPER NUMBER
WASHINGTO	ON. DC 20006-1021		2811	<u> </u>

DATE MAILED: 10/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

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Advisory Action

Application No.	Applicant(s)
10/623,655	HAMADA, TOMOJI
Examiner	Art Unit
Junghwa M. Im	2811

Before the Filing of an Appeal Brief	Examiner	Art Unit				
	Junghwa M. Im	2811				
		<u> </u>				
The MAILING DATE of this communication appe		•	ress			
THE REPLY FILED 009/28/2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. 1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:						
a) The period for reply expiresmonths from the mailing date of the final rejection. b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In ne event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TW MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).						
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL						
2. The Notice of Appeal was filed on <u>9/26/2005</u> . A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).						
AMENDMENTS 3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will <u>not</u> be entered because (a) They raise new issues that would require further consideration and/or search (see NOTE below);						
 (b) ☐ They raise the issue of new matter (see NOTE below); (c) ☐ They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or 						
(d) They present additional claims without canceling a		ejected claims.				
NOTE: (See 37 CFR 1.116 and 41.33(a)) 4. The amendments are not in compliance with 37 CFR 1.		ompliant Amendment	t (PTOL-324)			
5. Applicant's reply has overcome the following rejection(s		omphant Amenamen	. (1 102 024).			
6. Newly proposed or amended claim(s) would be a the non-allowable claim(s).		e, timely filed amendn	nent canceling			
7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended. The status of the claim(s) is (or will be) as follows: Claim(s) allowed:						
Claim(s) objected to: Claim(s) rejected: Claim(s) withdrawn from consideration:	•					
AFFIDAVIT OR OTHER EVIDENCE						
8. The affidavit or other evidence filed after a final action, b because applicant failed to provide a showing of good ar and was not earlier presented. See 37 CFR 1.116(e).						
 9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1). 10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached. 						
REQUEST FOR RECONSIDERATION/OTHER 11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.						
12. Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s)						
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Continuation of 11. does NOT place the application in condition for allowance because. Applicant mainly argues that "Rather, conductive layer 216 remains a single layer, albeit one having an opening therethrough, and accordingly, while this conductive layer can arguably be said to correspond to a single electrode." First, note that the conductive layers 216 are connected to the vias with a different configuration. In detail, the conductive layer on the left are connected to the vias 126 while the one on the right to the vias 232. And the vias 126 are to form a waveguide extension to ground (col. 5, lines 15-23), while the vias 232 are for signal routing to the circuit 208. Therefore, the conductive layers on the right and the left work as two different conductive path, since the conductive layer on the left is a conductive path to ground while the one on the right is used for signal routing. Furthermore, Huang discloses that "any conductive layer may be selectively patterned to prove DC bias and signal routing" (col. 4, lines 47-49), implying that the conductive layer can work as conductive paths (electrodes) for several signals.